

## REMARKS

Claims 1-19 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By separate letter, new corrected drawings have been provided in order to overcome the objections of the Examiner.

It is respectfully submitted that the drawings are now free from informalities.

Turning now to the art rejections, Claims 1, 2, 7, 8, and 13 were rejected under 35 U.S.C. § 103 as being unpatentable over Nishizawa in view of Ouyang.

These rejections are respectively traversed.

It is respectfully submitted that Nishizawa does not disclose or suggest the presently claimed invention including the gate of the output transistor being directly coupled to a first conductor to receive an input voltage as defined in the various forms in independent Claims 1, 2, 7, 8, and 13.

Nishizawa discloses that transistor 1 is connected to element 16 which in turn is connected to transistors 14 and 15.

Consequently, there is no direct input.

Ouyang does not disclose or suggest the presently claimed invention including the gate of the output transistor being directly coupled to a first conductor to receive the input voltage as defined in the various forms in independent Claims 1, 2, 7, 8, and 13.

Ouyang discloses that MOSFET 18 is connected between voltage pump 48 and fold-back current adjuster 44.

Consequently, there is no direct connection to received the input voltage.

Applicants appreciate the indication that if Claims 14-19 were rewritten in independent form including the limitations of the base claim and any intervening claims, these claims would be allowable.

By the instant amendment, Claims 14, 15, and 16 have been placed in independent form including the limitations of the base claim and any intervening claims.

Additionally, Applicants appreciate the indication that Claims 3-6 and 9-12 are allowed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including

extension of time fees, to the deposit account of Texas Instruments Incorporated,  
Account No. 20-0668.

Respectfully submitted,



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